

IN THE TITLE:

Please cancel the Title and substitute a new Title as follows:

A' ~~THREE-DIMENSIONAL GRAPHICAL ICON APPEARANCE IN DISPLAYS OF~~
~~ELECTRONIC DEVICES~~

IN THE SPECIFICATION:

At page 4, the paragraph beginning at line 18 has been rewritten as follows:

A2 Fig. 2E shows a "butterfly" icon, again illuminated from above, and made up of alternating light and dark vertical stripes, according to the present invention. Fig. 2F shows an enlargement of Fig. 2E in order to better appreciate the changing of the vertical stripes. For instance, a first dark stripe 24 at the left edge stays dark throughout the full extent thereof. The same can be said for the second stripe 26, which is a light-shaded stripe that does not change throughout its extent. A third stripe 28 which is dark shaded also does not change and remains dark throughout. A fourth stripe 30 is light colored for most of its extent, but includes a short dark portion near the top and which indicates a shadow adjacent an edge of the icon of the butterfly, i.e., a shadow under the tip of its left wing. The light-shaded vertical stripe 30 starts out as light shaded and then changes to dark shaded and changes back again to light shaded. A next stripe 32 is dark shaded for most of its extent, but has a short part near the top that is used to indicate a highlight at the top tip of the left wing of the butterfly icon.

This is an instance of a dark-shaded stripe changing from dark to light and back to dark again to indicate a highlight adjacent another edge of the icon of the butterfly. Altogether, these

A2 alternating light and dark stripes with parts indicating shadows and highlights provide the icon with a strong three-dimensional appearance.

At page 5, the paragraph beginning at line 3 has been rewritten as follows:

A3 Fig. 3 shows the communication device of Fig. 1 as schematic block diagram. The previously-mentioned display 14, keypad 16, microphone 18 and speaker 20 are shown to the right of Fig. 3. They are connected to an input/output (I/O) port 34 of a signal processor 36 which is inside of the device 10. The I/O device is also connected to the previously-mentioned antenna 38 through a receiver 40 and demodulator 42, as well as a modulator 44 and transmitter 46.
